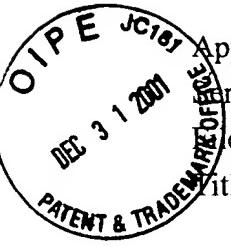


S/N 08/984,563

PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Jeffrey A. Mailloux et al.  
Serial No.: 08/984,563  
Filed: December 3, 1997  
Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

Examiner: Hong Kim

Group Art Unit: 2751

Docket: 303.623US4

JK  
1-10-02

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents  
Washington, D.C. 20231

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Group 2100

The Applicant has reviewed the Office Action mailed on July 16, 2001. Detailed comments to the Office Action are found in the Remarks section below. Please amend the above-identified patent application as follows, and consider the appended remarks.

IN THE SPECIFICATION

Please amend paragraph 1, on page 1 as follows:

This application is a divisional of application Serial No. 08/650,719, filed May 20, 1996,  
which is a Continuation-In-Part of application Serial No. 08/584,600, filed January 1, 1996, now  
U.S. Patent No. 5,966,724.

Please amend the paragraph beginning on page 38, line 11, as follows:

In column-based switching, switching between burst EDO and pipelined EDO modes is accomplished on successive /CAS cycles. Moreover, this type of switching may be accomplished on either read or write cycles, e.g., from a burst EDO read cycle to a pipelined EDO read cycle and [vise-versa] vise-versa, or from a burst EDO write cycle to a pipelined EDO write cycle and [vise-versa] vise-versa.

REMARKS

The Applicant has carefully reviewed and considered the Office Action mailed on July 16, 2001, and the references cited therewith. The Applicant's representative has reviewed the other art made of record by the Office, but believes that the cited references are more pertinent to the instant application. No claims have been amended, no claims are canceled, and no claims are